



FH915

FH915 radio transceiver is designed as universal license-free modem. It uses 902-928 MHz ISM (industrial, scientific and medical) license free USA band and 915-928 MHz Australian band with frequency hopping transmission techniques for excellent reliability in noisy plant environments and European CEPT license free 868-870 MHz band, allocated for narrow band telemetry, alarms and data transfer applications.

Thanks to its small size, and multiple functions, the FH915 is specifically well suited for amount of applications within industrial complexes, for various indoor as well as medium-range applications.

The unmatched features of FH915 include data scrambling, frequency hopping, user selectable transmit output power level, low power consumption sleep modes, repeater mode, and plug-and-play installation for remote terminals.

FH915 supports two separate Application Data and Maintenance modes of single UART serial port. The built-in software tools provide the wireless link testing, unit's status and error statistics monitoring. The firmware of the FH915 radio transceiver resides in a flash memory. The updating of the radio transceiver programs is entirely software-based. The flash memory is re-programmable through an UART interface.

The FH915 is developed for exacting customer needs and to have pin-to-pin compatibility with our OEM radios AW400Tx, AW400Rx, and AW100Tx.

FH915

Comments

General Specification

- Input Voltage: 4.0 V ± 5 %
- Power Consumption (average): 3 W transmit with 50% duty cycle (1 W TPO) 1 W - receive mode
- Operation Temperature: -40°C ... +60°C
- Storage Temperature: -40°C ... +80°C
- Dimensions: L: 80 mm x W: 46.5 mm x H: 7.6/9.5 mm
- Weight: 43 g

Features

- DSP-Modem
- Zero-IF Technologies
- 902-928 MHz (USA), 915-928 MHz (Australia), 868-870 MHz (EU) Frequency Ranges

1/0

- Up to 115200 bps Serial Interface Data Rate
- Embedded Firmware Compensation for Operation at Extremely Low and High Temperatures
- Compact Design

External Connectors:

RF Connector

PIN

#

J2 is Antenna Input / Output Connector: MMCX RIGHT ANGLE PCB JACK, AMPHENOL P/N 908-24100.

Description

Main Connector (J1)

Signal

Designator

16-Lead Header Connector, ECS Corp. P/N 9616-D1-01-03

Signal

name

Radio Transmitter Specifications

Component	Details
	+15+30 dBm in 1dB step/50 Ω (USA/Australia) +7+27 dBm in 1dB step/50 Ω (EU)

Radio Transceiver Specifications

Component	Details
Frequency Range	902-928 MHz (USA) 915-928 MHz (Australia) 868-870 MHz (EU) with 25/12.5 kHz CS
Link Rate, symbols/second	8000, 16000, 32000, 64000 (USA/Australia) 4800, 9600 (EU)
Carrier Frequency Stability	±1 ppm
Modulation	GMSK
Communication Mode	Half duplex, simplex

Radio Receiver Specifications

Component	Details
Receiver Sensitivity for GMSK (BER 1x 10⁴)	-110 dBm for 25 kHz CS -112 dBm for 12.5 kHz CS
Receiver Dynamic Range	-119 to -10 dBm

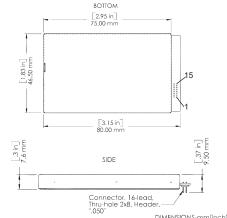
Modem Specification

	Component	Details
	Interface DSP	UART (serial port)
	Interface Connector	16-lead Connector
5	Data Speed of Serial Interface	9600 - 115200 bps
>	Data Rate of Radio Interface (USA/Australia)	8000 bps – GMSK 16000 bps –GMSK 32000 bps – GMSK 64000 bps – GMSK
ng	Data Rate Radio Interface (25 kHz CS)	9600 bps – GMSK
	Data Rate Radio Interface (12.5 kHz CS)	4800 bps – GMSK
ł	Forward Error Correction (FEC)	Convolutional code
ay	Data scrambling	Yes

1 GND GND Ground Signal and Chassis Ground 2 DSP UART 1 TXD Transmitted Data TTL Input Serial Data Input 3 DSP UART 2 RXD Received Data TTI Output Output for received serial data Control line can be used as a backup method for entering Command mode: (0V) – Maintenance Mode; (3.3V) - Data Mode DTR Data Terminal An internal 100K pull-up enables Data Mode if this 4 DPORT5 TTL Input or DP/MP Ready signal is left unconnected. Maintenance Mode is also accessible by transmitting an escape sequence. Used to control transmit flow from the user to the radio: 5 DPORT1 CTS Clear to Send TTL Output (0V) – Transmit buffer not full, continue transmittin (3.3V) – Transmit buffer full, stop transmitting In sleep mode, all radio functions are disabled consuming less than 50µA. An internal 10K pulldown wakes up the radio if this signal is left Sleeps/wakes unconnected. At wake up, any user programmed SI FFP 6 **TTI 11** radio TTL Input configuration settings are refreshed from flash Receive only memory, clearing any temporary settings that ma have been set: (3.3V) - Sleep Radio; (0V) - Wake Radio As an option could be used as TTL Input Line 1. Used by remotes to indicate that the remote has Data Carrier successfully acquired the signal from base station TTL Output 7 DPORT3 MDM GRN Detect (0V) – Carrier detected (synchronized) (3.3V) - No carrier detected (not synchronized) Gates the flow of receive data from the radio to the user on or off. An internal 10K pull-down enables data receive if this signal is left unconnected. In DPORT4 8 RTS Request to Send TTL Input normal operation, this signal should be asserted: (0V) - Receive data (RxD) enabled (3.3V) - Receive data (RxD) disabled Used to control transmit flow from the user to the radio: DPORT2 9 DSR Data Set Ready TTL Output (0V) - Receive buffer has data to transfer; (3.3V) - Receive buffer is empty Reset the radio by shortening this pin to the 10 RES CONT RESCONT Reset the radio TTL Input ground TTLO1 TTLOUT1 11 TTL Output Line 1 TTL Output Reserve line TTLO2 TTLOUT2 TTL Output 12 TTL Output Line 2 Reserve line GND GND Signal and Chassis Ground 13 Ground TTLI2 TTI IN 14 TTL Input line TTL Input An internal 100K pull-up resistor is applied. 15 VCC36 PWR Power Supply Regulated positive 4.0V DC from ext. Power Supply. External Regulated positive 4.0V DC from ext. Power Supply. 16 VCC36 PWR Power Supply External

Compliance

Parameter	Specification
FCC	Part 15.247
ETSI	EN 300 220-1, EN 301 489-1



DIMENSIONS-mm[inch]

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